

PATENT Docket No. Intel/17225

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tian et al.) I hereby certify that this paper is
) being deposited with the United
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Filed: October 2, 2003) mail in an envelope addressed to:
) Commissioner for Patents, P.O.
Assignee: Intel Corporation) Box 1450, Alexandria, VA 22313-
) 1450 on this date:
For: "Methods And Apparatus For	
Reducing Memory Latency In A Software) May 2, 2006
Application"	
) 1/2
Group Art Unit: 2121	Michael W. Zimmerman
	Registration No.: \$7,993
Examiner: Unknown) Agent for Applicant(s)
	0

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is a copy of a Written Opinion of the International Searching Authority for the International application no. PCT/US2004/032212.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

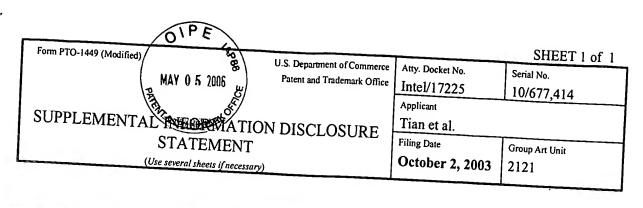
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By:

Michael W. Zimmerman Registration No.: 57,993

May 2, 2006

Agent for Intel Corporation



 COL	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) Submitted herewith is a correct of the page of the pag
C01	
 	Authority for the International application no. PCT/US2004/032212.
C02	application 10. PC1/US2004/032212.

EXAMINER

DATE CONSIDERED

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.